

Bio-Data

1.	Name	Dr. S. NAGARAJA RAO			
2.	Designation	Professor & HOD			
3.	Department	Electronics and Communication Engineering			
4.	Organization/Institute	G.Pulla Reddy Engineering College (Autonomous),Kurnool,A.P.			
5.	Qualifications	<p>Ph. D. in Image Processing From JNTUA, Anantapuramu, Andhra Pradesh. (Nov,2011)</p> <p>M. Tech in Digital Systems and Computer Electronics From J.N.T.University,Hyderabad (September, 1998)</p> <p>B.Tech in Electronics and Communication Engineering From KSRMCE, Kadapa affiliated to Sri Venkateswara University (April, 1990), A.P.</p>			
6.	Total Experience in Years	Teaching	Industry	Total	
		19Yrs	7yrs	26yrs	
7.	Papers Published in Journals	National		International	
		-		15	
8.	Papers Presented in Conferences	National		International	
		09		16	
9.	Ph.Ds/Projects Guided	PhDs : - 6 (Guiding)		Projects at Masters Level: - 20	
10.	Patents Filed	Title: A System for managing Power Conductors Application No. 201841042218			
11.	Funding/Research Grants received	AICTE funding of Rs 500000/-has been received to conduct International Conference under Grant for Organizing Conference scheme			
12.	Professional Memberships	<p>Life member of ISTE (Indian Society for Technical education) –Membership No. LM 85317</p> <p>Life member of Instrument Society of India (ISOI) -Membership No. LM 1666</p> <p>Life Member of Institute For Engineering Research and Publication (IFERP),Chennai</p> <p>IEEE member No. 92647048</p>			
13.	OTHER SKILLS/ACHIEVEMENTS	<ul style="list-style-type: none"> • Reviewer of International Journal of Research in Engineering and Technology(IJRET) (e-ISSN:2319-1163 & p-ISSN:2321-7308) • Editorial Member and Reviewer of American Journal of Computation, Communication and Control (ISSN:2375-3943) • Reviewer of American Journal of Science and Technology (ISSN:2375-3846) • Reviewer of Journal of Wireless Communications, Networking and Mobile Computing • Convener for AICTE sponsored Two Day International Conference on Computing, Communication, Electrical and Electronics Engineering (ICCCSEE- 2020) held on 10th & 11th January,2020. • Convener for International Conference on Advances in Signal Processing, Power, Embedded, Soft Computing, Communication and Control Systems (ICSPECS-2019) held on 11th & 12th January, 2019 at GPREC, Kurnool. • Convener for a two day National level workshop on “SoC Physical design using CADENCE tools” organized by Dept. of ECE, GPREC, Kurnool from 4-5th March,2016. • Workshop coordinator for Two week ISTE STTP on CMOS,Mixed Signal and Radio Frequency VLSI Design being organized by Dept.of ECE, GPREC under NMEICT,IIT Kharagpur from 30th Jan,2017 to 4th Feb,2017. • GPREC –APSSDC coordinator for implementing the Skill Development programmes. SPOC for t-SDI center, PMKVY-TI, CM’s Skill Excellent Center (CM’s SEC) at GPREC. <p>Some of the Research Papers which were selected for presentation in the Conferences conducted in Foreign Countries</p> <ul style="list-style-type: none"> • Invited for presenting the paper selected for IS & T/SPIE Symposium on Electronic Imaging -2007 held on 30-January’2007 in San Jose, California, USA • Invited for presenting the paper selected for WASET (World Academy of Science, Engineering and Technology), IEEE International Conference on computational Science & Engineering(ICCSSE) on 29th March’2009 held in Bangkok. • Invited for presenting the paper selected for MIC-CNIT 2009, Mosharaka International Conference on Communications, Networking and Information Technology, Amman, Jordan in Dec’2009. 			
14.	Contact details	<p>Cell no: +91-9440328621/8106367554</p> <p>Email ID: hodece@gprec.ac.in, drsrao.ece@gprec.ac.in</p> <p>URL: https://drsrao.wordpress.com</p> <p>ORCID: 0000-0003-3066-9533</p>			